Abstracts

A base-band to RF BiCMOS transmitter RFIC for dual-band CDMA/AMPS wireless handsets

K. Sahota, K. Gard, B. Walker, S. Szabo, Wenjun Su and E. Zeisel. "A base-band to RF BiCMOS transmitter RFIC for dual-band CDMA/AMPS wireless handsets." 2000 Radio Frequency Integrated Circuits (RFIC) Symposium 00. (2000 [RFIC]): 129-132.

A single chip, base-band to RF, BiCMOS transmitter RFIC (RFT3100) for dual-band CDMA/AMPS wireless handsets is presented. The RFIC contains base-band I/Q modulator, UHF VCO buffer, IF PLL, VCO, IF and RF VGAs, image rejection RF upconverter, dual driver amplifiers for cellular and PCS bands, and a three wire serial interface to control the chip. The chip operates from a 2.7 to 3.3 V supply, a temperature range of -30 to 85/spl deg/C, and is packaged in a 32 lead 5/spl times/5 mm bump chip carrier (BCC) package. The chip is fabricated using a 18 GHz (analog NPN ft), 0.5 um BiCMOS process.

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